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2. The method of claim 1 wherein writing pixel data to said first memory location

includes writing pixel data to a first virtual memory location.

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5. The method of claim 1 wherein generating said memory address for said second

memory location includes transforming the addresses of said pixel data at said first memory
location to addresses at said second memory location.

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9. The method of claim 1 wherein writing said transformed pixel data from said first

memory location to said second memory location includes writing said pixel data from said first
memory location associated with a first transfer function to said second memory location
associated with a second transfer function.

11. An article comprising a medium storing instructions that enable a processor-based

system to:

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write pixel data to a first memory location;

perform a first pixel transformation at said first memory location;

generate a memory address for a second memory location;

write said transformed pixel data from said first memory location to said second
memory location; and

transfer said pixel data to a memory controller using a memory controller client.

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19. The article of claim 11 further storing instructions that enable the processor-based

system to write said pixel data from said first memory location associated with a first transfer
function to said second memory location associated with a second transfer function.

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22. The system of claim 21 wherein said first memory controller client selectively

forwards pixel data and addresses to one of a plurality of transfer functions and said second
controller client receives pixel data with new addresses from said plurality of transfer functions.